

FIG. 1 PRIOR ART

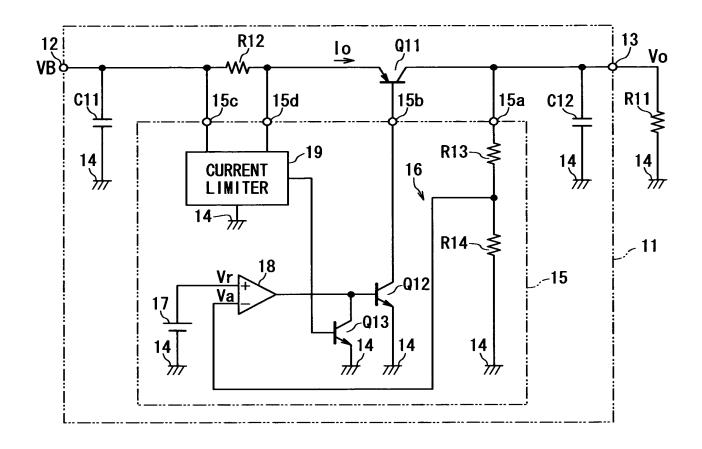
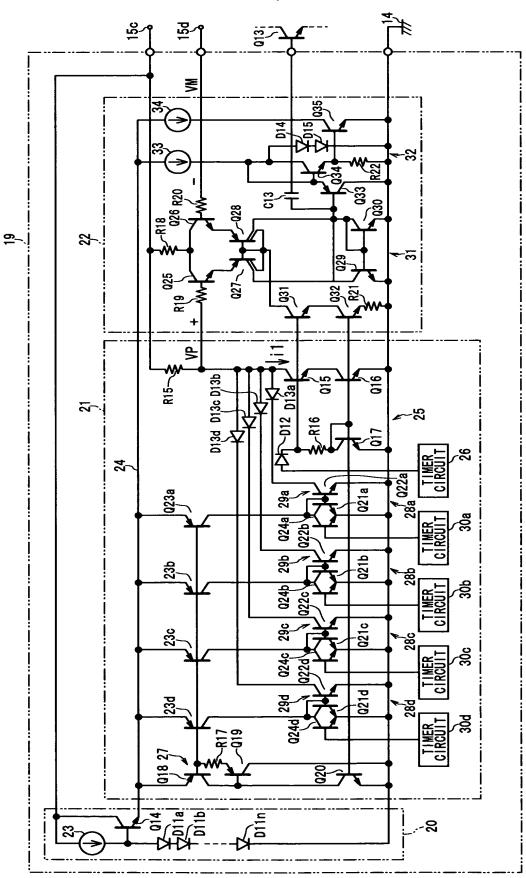


FIG. 2



F16.3

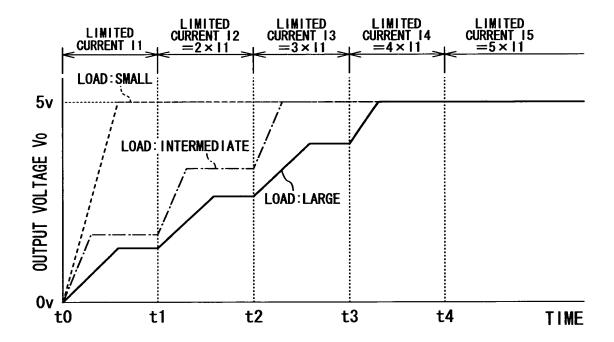


FIG. 4

FIG. 5A
LIMITED CURRENT
100mA

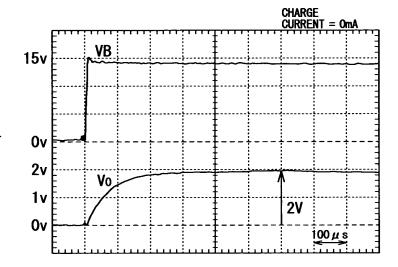


FIG. 5B
LIMITED CURRENT

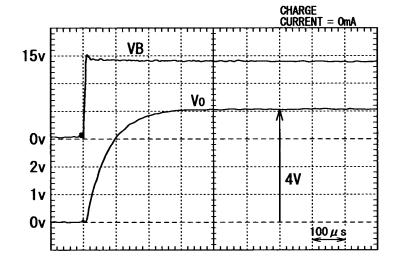


FIG. 5C
LIMITED CURRENT
400mA

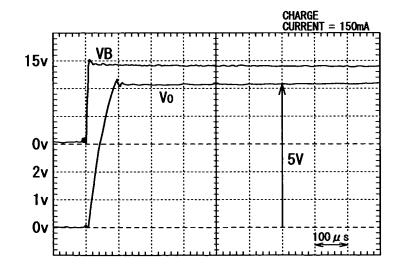


FIG. 6A
LIMITED CURRENT

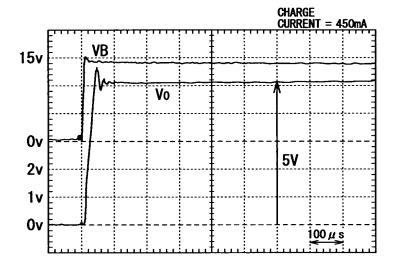


FIG. 6B

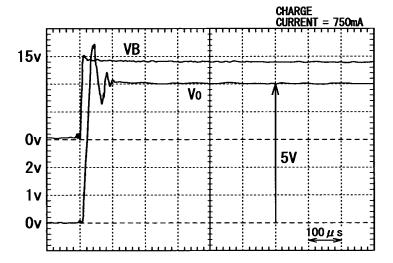
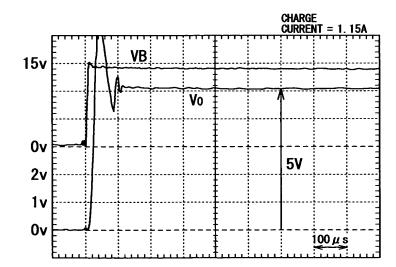


FIG. 6C
LIMITED CURRENT
1. 4A



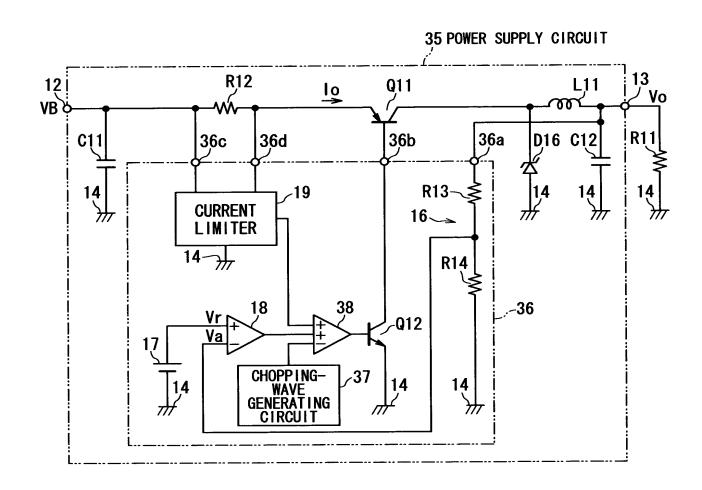
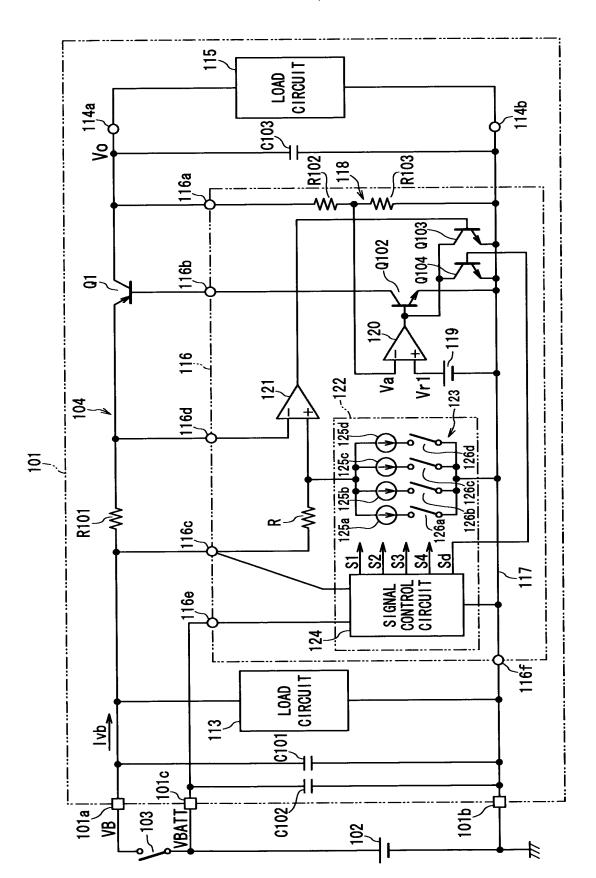


FIG. 7



F1G. 8

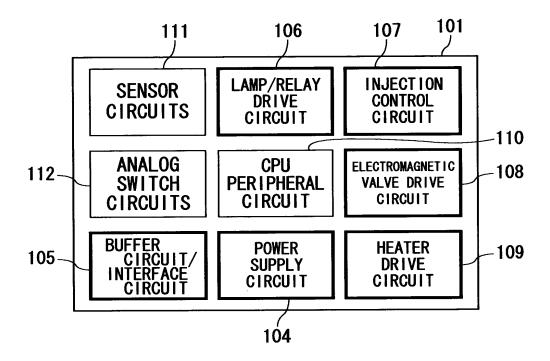


FIG. 9

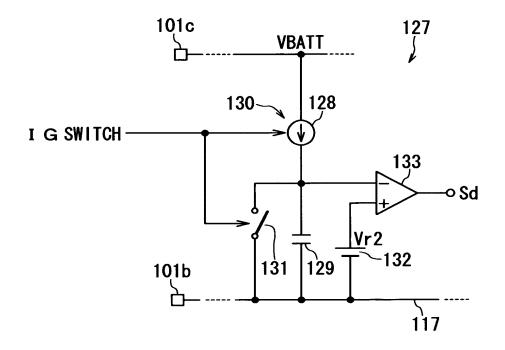


FIG. 10

FIG. 11A

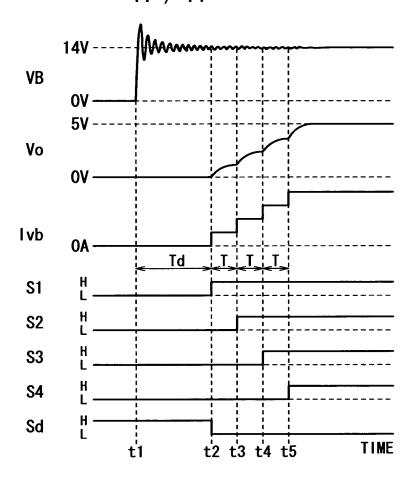
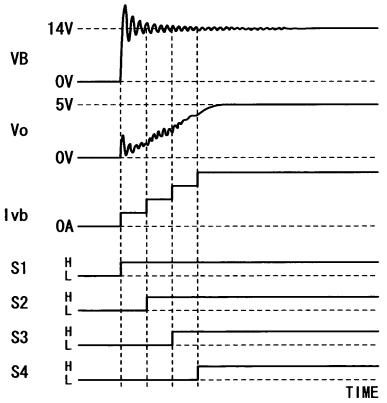


FIG. 11B



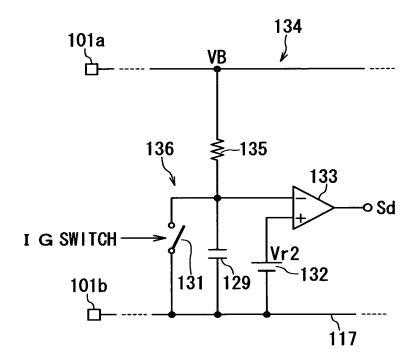


FIG. 12

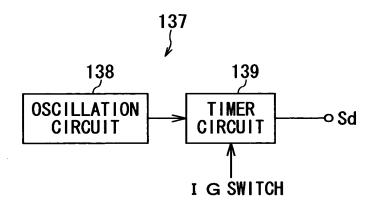


FIG. 13

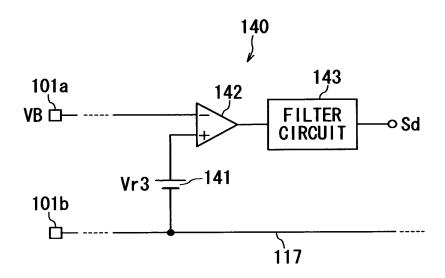


FIG. 14